

Micron Patent Docket Records

1 Records - Sorted by Docket Number

1379

<u>FileStatus:</u>	<u>TypeDesc:</u>	<u>Serial N:</u>	<u>Confr No:</u>	<u>Patent No:</u>
Filed	Invention Disclosure			

A NOVEL RAISED SOURCE DRAIN STRUCUTRE UTILIZING A POCKET JUNCTION

<u>Company:</u>	<u>Dept:</u>	<u>Assignee:</u>	<u>Cntry Origin:</u>	<u>Project:</u>	<u>Class:</u>
Micron Technology	899		US	GENERAL	
<u>AttyAgnt:</u>	<u>OADcktNo:</u>	<u>Assignd:</u>	<u>Firm:</u>	<u>Priority:</u>	
Dickos	98095		Kirkpatrick Lockhart	Normal	
<u>Disclosed:</u>	<u>Filed:</u>	<u>Issued:</u>	<u>ExpireDate:</u>	<u>Inactive:</u>	<u>Abandon:</u>
					<u>WaiverDate:</u>

Priority Date: Public Date: Frgn App No: Frgn Dckt No: Frgn Pat No: Purchased: Orginal Assignor:

Inventor(s):

M-0,003,113 : FERNANDO GONZALEZ
M-0,012,645 : CHANDRA MOULI

File Actions:

Invention Disclosure Docketed		
Assign to Patent Committee		
Patent Committee Disclosure Review		
Assign to Attorney/Agent		
Application First Draft		
Invntr First Draft Review		
Application Second Draft		
Invntr Second Draft Review		
Application Third Draft		
Invntr Third Draft Review		
Application Fourth Draft		
Invntr Fourth Draft Review		

Due Date

Completed

Classification Categories

<u>Level1:</u>	<u>Level2:</u>	<u>Level3:</u>	<u>Level4:</u>
<input checked="" type="checkbox"/> Semi Structure	Transistor	Field Effect	Gate
<input type="checkbox"/> Semi Structure	Transistor	Field Effect	Channel

filed appln - recvd. Recorded assign. o.a. amend due	- recvd restriction requirement.....election due field amend	- filed election rcd
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Patent Family:

<u>DocketNo:</u>	<u>Application Type:</u>	<u>FileStatus:</u>	<u>Filed:</u>	<u>Issued:</u>	<u>Patent No</u>	<u>AttyAgnt:</u>
-1379.00/US	Original Application	Pending				Dickos
-1379.01/US	Divisional	Pending				Dickos
-1379.02/US	Divisional	Pending				Dickos
-1379.03/US	Divisional	Pending				Dickos
-1379.04/US	Divisional	Pending				Dickos
-1379.05/US	Divisional	Pending				Dickos